

CLAIMS

What is claimed is:

1. An apparatus for sensing remote load voltages, comprising:
a power converter;
a plurality of remote loads, each remote load located in a loop connected to the power converter; and
a feed back loop connected to the power converter, the feed back loop being physically adjacent to the power converter, wherein the feed back loop further comprises a first path and a second path, and the first path and the second path are in parallel.
2. The apparatus of claim 1, wherein the first path further includes a low-pass filter.
3. The apparatus of claim 1, wherein the first path further includes a high-pass filter.
4. The apparatus of claim 1, wherein the first path further includes a band-pass filter.
5. The apparatus of claim 1, further comprising an error amplifier connected to the feed back loop.
6. The apparatus of claim 5, wherein the error amplifier has a gain defined by $G_{av} = (N * K_a) * (\text{weighted average individual loop gains})$
wherein,
 G_{av} is the average gain of the error amplifier,
 N is the number of loops, and
 K_a is a constant gain adjustment factor.

7. The apparatus of claim 1, wherein the first path further comprises a capacitor-resistor network.
8. A method for sensing remote load voltages comprising the steps of:
 - connecting a remote load to a loop to a power converter;
 - devising an impedance for a feed back loop according to a weighted factor for the feed back loop; and
 - connecting the feed back loop to the power converter, wherein the feed back loop is physically closer to the power converter than the remote load.
9. An apparatus for sensing remote load voltages, comprising:
 - means for converting power;
 - a plurality of means for consuming power, each means for consuming power being located in a loop connected to the means for converting power; and
 - means for providing feed back to the means for converting power, the means for providing feed back being physically adjacent to the means for converting power, wherein the means for providing feed back further comprises a first conducting means and a second conducting means, and the first conducting means and the second conducting means are in parallel.